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09 836286
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Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED <i>KS</i>	O.I.P.E. <i>ME</i>	PATENT DATE
TRV	G.A. <i>G.M.</i>	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT/763	EXAMINER
09/836286	F	438	706	<i>2583</i>	<i>Deo</i>
APPLICANTS: Hidetaka Nambu (claim 1)					
TITLE: Semiconductor device and manufacturing method of the device					
PTO-2040 12/89					

ISSUING CLASSIFICATION			
ORIGINAL		CROSS REFERENCE	
CLASS	SUBCLASS	CLASS	SUBCLASS (CONT. OF CLASSIFICATION)
INTERNATIONAL CLASSIFICATION			

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			NOTICE OF ALLOWANCE	
	Sheet Draw.	Fig. Draw.	Print Fig.	Total Sheets	
	The term of this patent subsequent to _____ (date) has been disclaimed.			Amount Due	
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